

LEVEL 2

LABORATORY TRAINING ON

SEMICONDUCTOR PACKAGING

2024

CO-ORGANIZED BY



All registrants of this workshop will get a full waiver on the registration fee for the IC-MNSS held from 10-12 July at IISc, Bengaluru.

Click <https://icmnss.in>
For more details



DATES:
06 to 09, JULY 2024



CMTI
Bengaluru



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Entry is limited to the first 50 participants only

YESTERYEAR HIGHLIGHTS



The **Level 2 Laboratory Training On Semiconductor Packaging** was initiated in 2023 in collaboration with CMTI, ISSS and STARC. The first edition was held from **27 February to 3 March 2023**. Around 45+ participants from Industry, academia and the student community took part in this workshop. The workshop also consisted of 19 keynote lectures and a visit to the STARC MEMS facility. The Eyes-On & Hands-On training consisted of several packaging and characterization techniques, from dicing to sealing. The participants of the workshops expressed positive feedback and suggested more such training to benefit the semiconductor community.

REVIEWS

Very Good!!

INTERACTIVE HANDS ON SESSIONS
INFORMATIVE PRESENTATIONS
LEARN IT PROCESS FLOW OF SEMICONDUCTOR PACKAGING

It is a nice workshop to establish new techniques for packaging of semiconductor devices

It's a very informative workshop

I will request to CMTI, please try to provide this training every year and try to accumulate & advertise among all IITs & NITs and other engineering college.

Thank you

It was a great experience of the lectures, work stations and BTD. All the technicians at BTD were so well structured and behaved. Thank you everyone and all the very best to CMTI.

Practical Hands On may be offered to feel and experience the experiences of CMTI in problem solving experiences. CMTI facilities and future technologies very useful to ignite new brains like school geer from class VIII to XII student for free to create more bent of mind and understanding for future readiness and technology need to work upon.

Great learning from all of the speakers, especially on packaging on Day 3 & 4 is more connecting on advanced packaging.

I got to increase my knowledge two to three times. Thanks a lot for my Director and CMTI Director and whole team

...was a laboratory training on semiconductor packaging have provided very informative lectures by the faculty. I would like to thank and congratulate CMTI and their team for very nice arrangements and other things.

The workshop was organized and followed flawlessly. The time dedicated to lectures could have been reduced and given to the laboratory sessions.

Overall, the efforts from all the members of CMTI and experts are appreciable.

It was great learning at CMTI. Thank you for this opportunity!






EVENT SCHEDULE

DAY 01 06.07.2024	09:00	Prof. M M Nayak
	10:00	CeNSE, IISc
	10:00	Dr. Ravi Meduri
	11:00	STARC, DRDO

DAY 02 07.07.2024	09:00	Dr. Arun Chandrashekar
	10:00	Intel
	10:00	
	11:00	

DAY 03 08.07.2024	09:00	Dr. Pradeep Vempaty
	10:00	Micron
	10:00	Dr. Amrit Ambirajan
	11:00	IISc
	14:00 17:00	VISIT TO STARC

DAY 04 09.07.2024	09:00	
	10:00	
	10:00	Dr. Meena Mishra
	11:00	SSPL

ALL DAYS	11:00	MORNING HIGH TEA	
	11:30		
	11:30	MORNING LAB SESSION	
	13:00		
	13:00	LUNCH	
	14:00		
	14:00	AFTERNOON LAB SESSION	
	17:00		
17:00	EVENING HIGH TEA		
17:30			

This training will include expert talks and lectures from leading industry professionals and experienced academic faculty. It covers the latest trends and recent advancements in advanced packaging technologies with future perspectives

Why this workshop?

What will be covered?

EYES-ON & HANDS-ON PROCESSES

- | | |
|-------------------|------------------|
| Wafer dicing | Wafer bonding |
| Wire bonding | Electroplating |
| Bond testing | E-beam welding |
| Flip chip bonding | Elec. |
| X-Ray Inspection | Characterization |

COURSE FEE*

Industry: 35400 INR
R&D/Faculty: 23600 INR
Students: 11800 INR

FOR ISSS MEMBERS*

Industry: 29500 INR
R&D/Faculty: 17700 INR
Students: 9440 INR

Fee?

* Inclusive of 18% GST

Register?

REGISTRATION



PAYMENT

